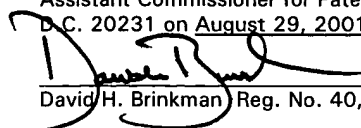




CERTIFICATE OF MAILING

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David H. Brinkman Reg. No. 40,532

PATENT

Serial No.: 09/898,601
Confirmation No.: 3051
Filed: July 3, 2001
Group Art Unit: 1732
Applicants: Austin et al.
Title: **METHOD FOR VACUUM ENCAPSULATION OF SEMICONDUCTOR CHIP PACKAGES**
Our Ref. No.: NOR-865B

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Washington, D.C. 20231

Sir:

INFORMATION DISCLOSURE STATEMENT

In accordance with the duty of candor and good faith imposed by 37 C.F.R. §1.56 and means of complying therewith according to 37 C.F.R. §§1.97 and 1.98, the references listed on the attached form PTO-1449 are called to the attention of the United States Patent and Trademark Office in connection with the above-identified patent application. The instant application is a divisional of co-pending U.S. Serial No. 09/322,416, filed May 28, 1999, which is a continuation

of U.S. Serial No. 09/187,617, filed November 6, 1998. The cited references were cited by the Applicants and/or the Examiner in U.S. Serial No. 09/322,416 and U.S. Serial No. 09/187,617. Since priority is claimed in the instant application to Serial No. 09/322,416, copies of the references are not being supplied herewith under the authority of 37 C.F.R. §1.98(d).

No admission is made that the cited art represents the prior art or that the cited art is the most material art.

The Examiner is urged to consider the cited references listed on the attached form PTO-1449, and to make an independent decision with respect to their materiality.


It is submitted that the cited references do not disclose or render obvious the subject matter claimed in the present application.

If any additional fees are necessary to complete this communication, please apply them to Deposit Account 23-3000.

Respectfully submitted,

WOOD, HERRON & EVANS, L.L.P.

BY



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